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# Optimal Sequence Estimation for Convolutionally Coded Signals With Binary Digital Modulation in ISI Channels

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## *Optimal Sequence Estimation for Convolutionalfy Coded Signals with Binary Digital Modulation in lSI Channels*

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ABSTRACT: *Decoding convolutional codes with binary digital modulation in intersymbol interference (ISI) channels is studied. The receiver structure is a whitened matched filter (WMF)* whose transfer function is determined by the ISI channel. Decoding of the output sequence can *be performed in two steps or one step. The two-step decoding first decodes the lSI corrupted coded sequence back to the lSIfree coded sequence which is then decoded back to the uncoded message sequence. For one-step decoding, the entire encoder-channel-receiver system is modeled as a new encoder with combined memory length of the memory lengths of the original encoder and the channel, and followed by a weighted summation mapping from the binary symbols to real number symbols. The weighting coefficients are determined by the channel characteristic. In both two-step and one-step decoding, the Viterbi algorithm (VA)* is *used to*  perform the maximum likelihood decoding. Decoding error probability and complexity of both *methods are analyzed, simulated and compared.* 

#### *I. Introduction*

A maximum likelihood sequence estimator (MLSE) consisting of a whitened matched filter followed by a Viterbi decoder was developed by Forney (1) for PAM channels with lSI. Since then, many papers have proposed a variety of schemes to reduce the complexity of the Viterbi algorithm (2-5). But all the above schemes are for an uncoded input sequence. Unlike uncoded input symbols, the coded symbols are correlated. However, the derivation of the WMF does not require the independence of the input symbols (1). Therefore, WMF structure is also optimal to coded input sequence. This paper describes how to apply the MLSE to convolutionally coded signals.

Previously, two schemes of using the Viterbi algorithm (VA) to decode lSI channels with convolutionally coded input sequence have been proposed (6, pp. 284-287). One of them does not use a whitening filter, therefore the output noise samples are not statistically independent. This poses a great difficulty to the analysis of the receiver performance. As a result no bit error rate expression was presented. The other receiver which does use the WMF appeared as a problem in (6, p. 298). However, no analysis or simulation has been found in the literature.

After Ungerboek invented the famous trellis coded modulation (TCM) (7),

several authors applied the Viterbi algorithm to decode the lSI contaminated TCM signals  $(2, 8-10)$ . Their receivers use a WMF. Therefore, the Viterbi algorithm is applied to the combined super trellis to decode the received signal sequence. To reduce the computational complexity of the Viterbi algorithm in the super trellis, the above papers explored a variety of reduced-state Viterbi algorithms. Duell-Hallen and Heegard (2) proposed a delayed decision feedback algorithm which is a reduced-state Viterbi algorithm with feedback incorporated into the structure of path metric computations. Chevillat and Eleftheriou (8) and Eyuboglu and Qureshi (9) used set partitioning to reduced the number of states of the super trellis. Zou and Weinrichter (10) simply truncated the lSI coefficients to reduce the number of states. All these algorithms can achieve a certain compromise between complexity and error performance. However, they all are dealing only with TCM signals in lSI channels.

This paper deals with the situation of decoding convolutional codes in lSI channels. This situation arises in systems that use convolutional codes and binary modulation such as BPSK, DPSK and BFSK. In these cases, a transmitted symbol corresponds to one bit of the coded bit sequence. The finite state machine (FSM) representing the transmitter-channel-WMF combination is a one-dimensional transversal filter. However, in the case of TCM, since a symbol is chosen from the two-dimensional signal constellation using Ungerboek's set partitioning method, the FSM is a complex (or two-dimensional) transversal filter. Therefore, despite the similarity between TCM and original convolutional codes, neither the methods nor the results in  $(2, 8-10)$  are applicable to the cases of convolutional codes with binary modulations.

This paper is to fill this gap. A well-structured combined FSM will be derived. This combined FSM will be expressed by a generator matrix which is derived from the code generator and the channel FSM model. This allows the results to be applied to any combination of convolutional codes of any code rate and channel FSM model. Based on this combined FSM model the super trellis can be easily constructed and error performance and complexity of the Viterbi algorithm can be easily evaluated. Any reduced-state Viterbi algorithm can also be applied and their performance can be easily analyzed using previous results in the literature.

The paper is organized as follows. In Section II, the receiver structure is presented. Section III describes the combined FSM model and the combined trellis for one-step decoding. Section IV is the performance analysis and comparison. Section V concludes the paper.

#### *II. Receiver Structure*

The maximum likelihood receiver structure for an lSI channel with uncoded inputs consists of a WMF and a Viterbi decoder (1). With a convolutional encoder at the front end the cascade of the encoder, channel (baseband) and the WMF is shown in Fig. 1.

The input and output symbols of the encoder are binary (0,1). In a practical system the  $(0,1)$  alphabet is converted to a  $(-1,1)$  alphabet since the latter needs 3 dB less average bit energy for the same error probability. However, it can be



FIG. 1. Cascade of the encoder, the channel and the whitened matched filter.

shown that the effect of the input level shifting on the output of the FSM is merely a level shifting/scaling of the output signals, which does not affect the error performance analysis. Therefore, the converter is omitted in the model of this paper.

The channel is characterized by a square-integrable impulse response  $h(t)$ of length *L* symbol intervals, i.e.  $h(t) = 0$  for  $t > LT$  and  $t < 0$  and  $||h||^2 = \int_{-\infty}^{+\infty} h^2(t) dt < \infty$ . A white Gaussian noise  $n(t)$  of zero mean and doublesided spectral density  $N_0/2$  represents noise entering at various points of the channel. The transfer function of the whitening filter is  $1/f(D^{-1})$ , where  $f(D^{-1})$  is one of two factors of the spectrum of the channel:

$$
r(D) = f(D)f(D^{-1}),
$$
\n(1)

where

$$
r(D) = \sum_{k=-\nu}^{\nu} r_k D^k \tag{2}
$$

and  $v = L - 1$ . We use  $r(D)$  to denote the D-transform of the autocorrelation function of  $h(t)$ ,

$$
r_{k} = \begin{cases} \int_{-\infty}^{\infty} h(t)h(t - kT) dt & |k| \le L - 1 \\ 0 & |k| \ge L. \end{cases}
$$
 (3)

The cascade of the channel and the WMF can be lumped in a FSM model whose transfer function is  $f(D)$  of degree v, another factor of the  $r(D)$  (1):

$$
f(D) = f_0 + f_1 D + \dots + f_v D^v \tag{4}
$$

Thus the entire system can be shown as in Fig. 2.

The code is an  $(n, b, m)$  code of rate  $R = b/n$ . The transfer function matrix  $G(D)$ of the encoder is defined as

$$
G(D) = \begin{bmatrix} g_1^{(1)}(D) & g_1^{(2)}(D) & \dots & g_1^{(n)}(D) \\ g_2^{(1)}(D) & g_2^{(2)}(D) & \dots & g_2^{(n)}(D) \\ \vdots & \vdots & & \vdots \\ g_b^{(1)}(D) & g_b^{(2)}(D) & \dots & g_b^{(n)}(D) \end{bmatrix}
$$
(5)



FIG. 2. Cascade of the encoder and the finite state machine model.

in which  $g_l^{(j)}(D)$  is the generator polynomial relating input  $u^{(j)}$  to output  $x^{(j)}$ , and  $g^{(j)}(D)$  has a maximum degree of *m*.

Corresponding to a b-dimensional input vector  $\mathbf{u}_k$ , there is an *n*-dimensional vector  $x_k$ . Their relation, in D-transform, is

$$
\mathbf{x}(D) = G^{\mathrm{T}}(D)\mathbf{u}(D) \tag{6}
$$

where all operations are in  $GF(2)$  field (modulo-2), and the superscript  $T$  denotes transpose of the matrix. The components of  $x_k$  are shifted sequentially into the FSM. In other words, the input sequence to the FSM is a one-dimensional sequence  $x(D)$ , thus the output sequences are

$$
y(D) = f(D)x(D)
$$
 (7)

and

$$
z(D) = y(D) + n(D) \tag{8}
$$

where all operations are in real number field **R**. We use  $n(D)$  to denote a white Gaussian noise sequence with zero mean and variance  $N_0/2$ .

**In** time domain the relation is a convolution:

$$
y_k = \sum_{i=0}^{v} f_i x_{k-i}
$$
 (9)

and

$$
z_k = y_k + n_k. \tag{10}
$$

#### *III. One-step Decoding*

Based on the diagram in Fig. 2, a straightforward decoding method would be a two-step decoding method. The two-step decoder decodes sequence  $z(D)$  back to  $x(D)$  which is then decoded back to  $u(D)$ . For maximum likelihood decoding, the first decoder (VAl) is a soft-decision Viterbi decoder (1) and the second one (VA2) is a hard-decision Viterbi decoder. VA2 cannot make use of any signal amplitude information which has been lost after VAL However, if we combine the encoder and the FSM together and apply a one-step Viterbi decoding to the super FSM, the signal amplitude information would be fully utilized in the decoding process.

In order to derive the one-step decoding algorithm, all inputs and outputs are grouped in vector forms. Corresponding to each b-dimensional input vector  $\mathbf{u}_k$ , there are *n*-dimensional vectors  $\mathbf{x}_k$ ,  $\mathbf{y}_k$  and  $\mathbf{z}_k$  at different stages' output. The relations between  $\mathbf{u}_k$  and  $\mathbf{x}_k$ ,  $\mathbf{y}_k$  and  $\mathbf{z}_k$  are straightforward (see Eqs (6), (8)).

However, the relation between  $y_k$  and  $x_k$  is not simple due to the sequential feeding of the  $\mathbf{x}'_k$ s components into the FSM. In fact, each  $\mathbf{y}_k$  involves current  $\mathbf{x}_k$ and at least one previous input, namely,  $\mathbf{x}_{k-1}$ . In general,  $\mathbf{y}_k$  is determined by  $\mathbf{x}_k$ ,  $\mathbf{x}_{k-1}, \ldots, \mathbf{x}_{k-j}$  as follows:

$$
\begin{bmatrix} y_k^{(1)} \\ y_k^{(2)} \\ \vdots \\ y_k^{(n)} \end{bmatrix} = \begin{bmatrix} 0 & \dots & 0 & 0 & f_0 & \dots & f_v \\ 0 & \dots & 0 & f_0 & \dots & f_v & 0 \\ \vdots & \vdots & & & \vdots & \vdots \\ f_0 & \dots & f_v & 0 & \dots & 0 & 0 \end{bmatrix} \begin{bmatrix} x_k^{(n)} \\ x_k^{(1)} \\ x_{k-1}^{(n)} \\ \vdots \\ x_{k-1}^{(1)} \\ x_{k-2}^{(n)} \\ \vdots \\ x_{k-2}^{(n)} \\ \vdots \\ x_{k-2}^{(n)} \end{bmatrix}
$$
 (11)

where  $\lambda = \lceil v/n \rceil$  and  $\eta = \lambda n - v + 1$ . Here,  $\lceil v/n \rceil$  denotes the smallest integer not less than  $v/n$ . In vector form the above equation can be written as

$$
\mathbf{y}_k = \mathbf{F}\mathbf{x}'_k \tag{12}
$$

in obvious notation.

Measured in number of input vectors, the memory length of the FSM is  $\lambda$ . The total memory length of the encoder-FSM cascade is therefore

$$
m' = m + \lambda. \tag{13}
$$

The dimension of the output vector  $x'_{k}$  is

$$
n' = n + v. \tag{14}
$$

Thus the system can be equivalently thought of as a new encoder  $G'(D)$  with a longer memory *m'* and a larger dimensional *(n')* output. The number of states of the encoder is  $S' = 2^{bm} = S2^{b\lambda}$ , where  $S = 2^{bm}$  is the number of states of the original encoder. The new encoder generates  $x_k$ ,  $x_{k-1}$ , ...,  $x_{k-1}$  simultaneously. They compose  $x'_{k}$ . The new generator matrix is

$$
G'(D) = [G_r(D), DG_r(D), \dots, D^{\lambda} \tilde{G}_r(D)],
$$
\n(15)

where  $G_r(D)$  is obtained by reversing the order of  $G(D)$ 's columns:



Fig. 3. Combined finite state machine model of the system of Fig. 2.  
\n
$$
G_r(D) = \begin{bmatrix} g_1^{(n)}(D) & g_1^{(n-1)}(D) & \dots & g_1^{(1)}(D) \\ g_2^{(n)}(D) & g_2^{(n-1)}(D) & \dots & g_2^{(1)}(D) \\ \vdots & \vdots & & \vdots \\ g_b^{(n)}(D) & g_b^{(n-1)}(D) & \dots & g_b^{(1)}(D) \end{bmatrix}.
$$
\n(16)

Then  $\tilde{G_r}(D)$  is formed from  $G_r(D)$  by dropping  $g_i^{(j)}(D)$  for  $i = 1, \ldots, b$  and  $j < \eta$ . The new encoder can be represented by

$$
\mathbf{x}'(D) = G^{\prime \mathrm{T}}(D)\mathbf{u}(D). \tag{17}
$$

Then  $x'_k$  is mapped into  $y_k$  through a weighted summation operation with the weighting coefficients  $f_0, f_1, \ldots, f_r$  (Fig. 3). Clearly this equivalent model is an FSM. The output vector  $y_k$  is determined by the state of the encoder  $G'(D)$  and the input vector  $\mathbf{u}_k$ . The sufficient statistic is  $\mathbf{z}_k = \mathbf{y}_k + \mathbf{n}_k$ , where  $\mathbf{n}_k$  is the white Gaussian noise vector. Its components are independent and one noise vector is independent from another.

#### *Example* I

A simple lSI channel and a simple code are chosen in this example for the purpose of illustration. The channel is the one-pole channel

$$
h(t) = \begin{cases} \beta e^{-\alpha t} & t \ge 0 \\ 0 & t < 0 \end{cases}
$$
 (18)

where  $\alpha > 0$ ,  $\beta > 0$ . It is a slowly decaying low-pass channel when  $\alpha$  is small. By choosing appropriate parameters and truncation (4), one possible transfer function *F(D)* of the FSM model could be

$$
f(D) = f_0 + f_1 D + f_2 D^2
$$
  
= 1 + e<sup>-0.5</sup>D + e<sup>-1.0</sup>D<sup>2</sup>  
= 1 + 0.607D + 0.368D<sup>2</sup>. (19)

Now consider a maximum  $d_{\text{free}}$  (3,2,1) code with

$$
G(D) = \begin{bmatrix} 1+D & D & 1+D \\ D & 1 & 1 \end{bmatrix}
$$
 (20)





FIG. 4. Figures for Example I: (a) Cascade of the encoder and the FSM; (b) Combined FSM.

and the encoder is cascaded with this channel as shown in Fig. 4(a), from which it is seen that

$$
y_k^{(1)} = f_0 x_k^{(1)} + f_1 x_{k-1}^{(3)} + f_2 x_{k-1}^{(2)},
$$
  
\n
$$
y_k^{(2)} = f_0 x_k^{(2)} + f_1 x_k^{(1)} + f_2 x_{k-1}^{(3)},
$$
  
\n
$$
y_k^{(3)} = f_0 x_k^{(3)} + f_1 x_k^{(2)} + f_2 x_k^{(1)}.
$$
\n(21)

The equivalent system model is shown in Fig. 4(b). The new encoder grows out of

the original encoder by adding one more memory element  $(\lambda = 1)$  and repeating the connections for the  $x_k^{(2)}$  and  $x_k^{(3)}$  one stage later to form the connections for  $x_{k}^{(2)}$  and  $x_{k}^{(3)}$ . The generator of the new encoder is therefore

$$
G'(D) = \begin{bmatrix} 1+D & D & 1+D & D+D^2 & D^2 \\ 1 & 1 & D & D & D \end{bmatrix}.
$$
 (22)

The code rate reduces to  $R' = 2/5$ . However, it is not the real rate in the system. The real rate is still 2/3.

Given the equivalent finite state machine, maximum likelihood (ML) estimation can be applied to the output sequence  $z(D)$  to estimate the input sequence  $u(D)$ . Extending the one-dimensional case in (1) to the multiple-dimensional case, it is easy to show that maximizing the likelihood is equivalent to minimizing the following quantity,

$$
M_p(\mathbf{u}(D)) = \sum_{k=1}^N M_b(\mathbf{u}_k, \mathbf{z}_k),
$$
 (23)

which is the path metric, where

$$
M_b(\mathbf{u}_k, \mathbf{z}_k) = \sum_{i=1}^n (z_k^{(i)} - y_k^{(i)})^2
$$
 (24)

is the branch metric. The path metric is the squared Euclidean distance between sequences  $y(D)$  and  $z(D)$ . The branch metric is the squared Euclidean distance between vectors  $y_k$  and  $z_k$ .

The Viterbi algorithm is an efficient means of implementing the strict ML estimation  $(6)$ . It will use the above metrics. For the equivalent  $(n', b, m')$  FSM, the *m'* most recent inputs determine a state of the FSM. Denote a state at any time  $k$  as  $\mathbf{s}_k$ ; then

$$
\mathbf{s}_k = (\mathbf{u}_{k-1}, \mathbf{u}_{k-2}, \dots, \mathbf{u}_{k-m'}). \tag{25}
$$

The trellis of the Viterbi algorithm has  $2^{bm}$  states. There are  $2^b$  transitions stemming from each state. Each transition is associated with an input vector  $\mathbf{u}_k$  and an output vector  $y_k$  as denoted by

$$
\mathbf{y}_k = y(\mathbf{u}_k, \mathbf{s}_k) = y(\mathbf{u}_k, \hat{\mathbf{u}}_{k-1}, \dots, \hat{\mathbf{u}}_{k-m'}).
$$
(26)

To calculate  $y_k$  one must first calculate  $x'_k$  from  $(\mathbf{u}_k, \hat{\mathbf{u}}_{k-1}, \dots, \hat{\mathbf{u}}_{k-m'})$  using (17) (modulo-2 arithmetic), then calculate  $y_k$  from  $x'_k$  using (12) (real number arithmetic).

Figure 5 shows trellises of the FSMs of Example 1. The Viterbi algorithm searches through the trellis for the ML path. For details of the well known Viterbi algorithm the readers are referred to (11) or other books on coding.



(c) Trellis for VA2 in two-step decoding

FIG. 5. Trellises for Example I.

#### *IV. Performance Analysis*

#### *4.1. Error probability*

The analysis for the one-step decoding is basically an extension from the onedimensional uncoded case in  $(1, 12)$  to the coded b-dimensional input *n*-dimensional output FSM case.

Following a derivation procedure similar to that in (1, 12), the upper bound of the bit error probability is found as

$$
P_b \leqslant \sum_{d \in D} Q \left[ \frac{d}{\sqrt{2N_0}} \right] \frac{1}{b} \sum_{\varepsilon \in E_d} W_H(\varepsilon) \left[ \prod_{i=0}^H \prod_{j=1}^b \frac{2 - |e_{u_i}^{(j)}|}{2} \right] \tag{27}
$$

where  $\varepsilon$  denotes an error event. Here,  $E_d$  is the set of error events which cause all incorrect paths at distance *d, D* is the set of all possible paths which are of distance *d* away from the correct path,  $W_H(\varepsilon)$  is the Hamming weight of the input error sequence  $e_u(D)$  which has a degree of *H*, and  $e_u^{(j)}$  is the *j*th component of the *i*th term in the error sequence  $e_u(D)$ . The  $Q(\cdot)$  function is the error function defined as

$$
Q(x) = \int_{x}^{\infty} \frac{1}{\sqrt{2\pi}} \exp\left[-\frac{x^2}{2}\right] dx.
$$

A distance *d* is associated with an error event and is evaluated by using error sequence  $e_y(D)$  in the following expression:

$$
d^{2}(\varepsilon) = \|\mathbf{e}_{y}\|^{2} = \sum_{i=0}^{K} \|\mathbf{e}_{y_{i}}\|^{2}
$$
  
= 
$$
\sum_{i=0}^{K} \sum_{j=1}^{n} (e_{y_{i}}^{(j)})^{2} = [\mathbf{e}_{y}^{T}(D^{-1})\mathbf{e}_{y}(D)]_{D=0}.
$$
 (28)

To evaluate the bound in (27), a simple but lengthy method is to perform a quasi-exhaustive search over main incorrect paths whose distances are close to the minimum distance  $d_{\min - \mu}$ . This is usually done by a computer. If the distance spectrum of the FSM is such that the  $d_{\min}$  is much smaller than other distances, then an approximation of Eq. (27) is given by

$$
P_{b1} \approx K_1 Q \bigg( \frac{d_{\min - u}}{2N_0} \bigg),\tag{29}
$$

where

$$
K_1 = \frac{1}{b} \sum_{\varepsilon \in E_{d_{\min}}}\left[W_H(\varepsilon)\prod_{i=0}^H\prod_{j=1}^b\frac{2-|e_{u_j}^{(j)}|}{2}\right].
$$
 (30)

This expression is obtained by simply ignoring all the terms in Eq. (27) except those involving the minimum distance  $d_{\min}-u$  of  $d(\varepsilon)$ . This approximation is called an upper estimate. It was shown that the upper bound asymptoticaIly tends to

upper estimate when  $N_0$  goes to zero (1). When  $d_{\min - u}$  is much smaller than other  $d(\varepsilon)$ , Eq. (27) is accurate. However when  $d_{\min-\nu}$  is not much smaller than other  $d(\varepsilon)$ , the estimate can be quite poor. Then a larger section of the distance spectrum needs to be included in the error probability calculation.

In two-step decoding, the BER expression for VAl is simply a special case of Eqs (29) and (30). In this case,  $b = 1$  and the input sequence is  $x(D)$ . Denoting the BER of the VA1 as  $P_{h-VA1}$ , then

$$
P_{b-\text{VAL}} \approx K_2 Q \bigg( \frac{d_{\min-x}}{\sqrt{2N_0}} \bigg),\tag{31}
$$

where

$$
K_2 = \sum_{\varepsilon \in E_{d_{\min-1}}} \left[W_H(\varepsilon) \prod_{i=0}^H \frac{2 - |e_{x_i}|}{2}\right].
$$
 (32)

The minimum Euclidean distance  $d_{\min-x}$  is different from  $d_{\min-x}$  in one-step decoding. The one-step decoding is conducted in the composite trellis which relates  $u(D)$  to  $y(D)$  directly. All the possible  $u(D)'s$ , but not all the possible  $x(D)'s$ , are tested. Thus  $d_{\min-x}$  is the minimum distance in the composite trellis. Here,  $d_{\min-x}$ corresponds to error events in  $\mathbf{u}(D)$  that produce the minimum distance between *y(D)'s.* In two-step decoding VAl and VA2 are conducted in two different trellises. The VA1 is conducted in the trellis relating  $x(D)$  to  $y(D)$ . All the possible  $x(D)$ 's are tested, in other words, there is no constraint on  $x(D)$ 's. The error probability is determined by the  $d_{\min-x}$  in the trellis. Also,  $d_{\min-x}$  corresponds to error events in  $x(D)$  that produce the minimum distance:  $d_{min-x}$  is the minimum distance purely due to the ISI channel and uncoded input sequences. Due to encoding those  $d_{\min - u}$ error events in  $u(D)$  do not necessarily generate those  $d_{\min-x}$  error events in  $x(D)$ .

The error probability expression for a hard decision Viterbi decoder is well known (11):

$$
p_b \approx \frac{1}{b} B_{d_{\text{free}}} 2^{d_{\text{free}}} p^{d_{\text{free}}/2},\tag{33}
$$

where  $d_{\text{free}}$  is the minimum distance between any two code words in the code generated by  $G(D)$ . Since convolutional code is linear,  $d_{\text{free}}$  is also the minimum weight of the nonzero code words in the code. Here,  $B_{d_{\text{free}}}$  is the total number of nonzero message bits on all weight  $d_{\text{free}}$  paths, and  $p$  is the channel transition error probability. However, this expression is only valid for an input sequence with random errors. The input sequence to the VA2 is the sequence  $\hat{x}(D)$  which is the output sequence of VA1. The errors in the input sequence of VAI, *z(D),* are random. But the errors in  $\hat{x}(D)$  are not completely random. As a matter of fact, the errors most likely occur as bursts of the length of the error events which cause  $d_{\min-x}$ . As a result, Eq. (33) is not accurate for VA2. Thus  $P_{b-YAZ}$  will be less than  $P_{b-\text{VA1}}$  and greater than that expressed by (33). The actual  $P_{b-\text{VA2}}$  depends upon the randomness of the errors in sequence  $x(D)$  and the error correcting capability of the code. Due to the vast difference in values predicted by (31) and (33), these

two bounds are so loose that they are not useful. The evaluation of  $P_{b-\text{VA2}}$  thus is better done by simulation.

#### *4.2. Coding gains*

To find coding gain (or loss) one needs to write all BER expressions in terms of the average bit energy to noise spectral density ratio  $E_b/N_a$ . The  $E_b = 0.5$  in our model since the input alphabet is (0, 1) where both symbols are equally likely. The bit-by-bit detection of a uncoded binary lSI-free PAM sequence has a BER of

$$
P_b = Q(\sqrt{E_b/N_0}).
$$

The MLSE of an uncoded lSI sequence has a BER of

$$
P_b = K_2 Q(d_{\min-x}/\sqrt{2N_0}) = K_2 Q(d_{\min-x}\sqrt{E_b/N_0}).
$$
\n(34)

For the coded sequence the  $E_s = RE_b$ , where *R* is the code rate and  $E_s$  is the average symbol energy. Thus the BER of the one-step decoding is

$$
P_{b1} \approx K_1 Q(d_{\min - u} \sqrt{RE_b/N_0}). \tag{35}
$$

Thus, by comparing (34) and (35), the asymptotical coding gain for one-step decoding is approximately  $(K_1 \text{ and } K_2 \text{ are ignored})$ 

$$
\gamma_1 \approx 10 \log \left( R d_{\min - u}^2 / d_{\min - x}^2 \right) \text{(dB)} \tag{36}
$$

where *R* accounts for the transmission energy reduction due to coding for a fixed energy per information bit.

For two-step decoding, analytical coding gain cannot be obtained due to the lack of error probability expression.

In the following analytical and numerical simulation results of the  $P<sub>b</sub>$  of two examples are given.

#### *4.3. Error probability of examples*

#### *Example 2*

This example assumes the same one-pole lowpass channel as in Example 1. The code has a generator

$$
G(D) = [1 + D + D2 + D3 + D6 1 + D2 + D3 + D5 + D6].
$$
 (37)

This is the popular (2, 1, 6) code which has been implemented as a VLSI chip by several manufacturers. This code has an  $d_{\text{free}} = 10$  and an asymptotical hard decision coding gain of 3.97 dB and an asymptotical soft decision coding gain of 6.97 dB.

#### *Example* 3

This example assumes the same code as in Example 2, but the channel is a magnetic recording channel or Lorentzian channel given by



FIG. 6. Bit error probability of Example 2.

$$
h(t) = \left[1 + \left(\frac{t}{\tau}\right)^2\right]^{-1} - \left[1 + \left(\frac{t - T_b}{\tau}\right)^2\right]^{-1} \tag{38}
$$

where  $T_b$  is the bit period. Assuming  $T_b = \tau$  then the truncated FSM transfer function (2 lSI terms kept) is (4)

$$
f(D) = 1 + 0.260D - 0.368D^2.
$$
 (39)

To determine the performance, the most important thing to do is to find all  $d_{\text{min}}$ . The  $d_{\min-x}$  is determined by the channel. By computer search using (28),  $d_{\min-x}$  of Example 2 is found to be 1.161, which corresponds to two error events:  $e_x(D) = \pm (1-D)$ . Here,  $K_2 = 1.0$  according to (32). The  $d_{\min-x}$  of Example 2 is found to be 2.335, which corresponds to two error events:  $e_u(D) = \pm (1-D)$ . Here,  $K_1 = 1$ . The  $d_{\min-x}$  of Example 3 is found to be 1.097, which corresponds to two error events:  $e_x(D) = \pm 1$ . Here,  $K_2 = 1.0$ . The  $d_{\min-x}$  of Example 3 is found to be 2.094, which corresponds to one error event:  $e_u(D) = 1$ . Here,  $K_1 = 0.5$ . Using these parameters the BERs and the coding gains can be determined. The estimated coding gains for Examples 2 and 3 are 3.06 (dB) and 2.61 (dB), respectively, using one-step decoding. They are slightly poorer than that of ISI-free, hard decision decoding. But it is about 3 to 4 (dB) poorer than that of lSI-free soft decision decoding. This means that the lSI causes degradation in coding gain.

Figures 6 and 7 show the analytical and simulation results of Examples 2 and 3. From the figures it is seen that the simulated  $P<sub>b</sub>$  of the one-step decoding is quite close to the prediction. Since the prediction is an upper estimate, the simulated  $P_h$ being slightly smaller is reasonable.

Figure 6 shows that the simulated asymptotical one-step coding gain for Example 2 is about 4 dB which is slightly larger than the prediction (3.06 dB). It also shows





FiG. 7. Bit error probability of Example 3.

that the simulated two-step coding gain is about 0.2 dB at  $E_b/N_0$  over 10 dB. The VA2 improves the BER of the VA1 by 2 dB, but it still can not compensate for the loss of magnitude information after VAL As a result, the one-step decoding is about 3.8 dB better than the two-step decoding in this example. However, when compared with lSI-free channel, soft decision case, the one-step decoding is still about 3 dB poorer. This 3 dB loss is caused by the lSI.

Figure 7 shows that the simulated asymptotical one-step coding gain for Example 3 is about 2.5 dB which is close to the prediction (2.61 dB). It also shows that the simulated two-step coding gain is about 1 dB at  $E_b/N_0$  over 10 dB. The VA2 improves the BER of the VAl by about 3 dB. The one-step decoding is about 2 dB better than the two-step decoding in this example. However, when compared with ISI-free channel, soft decision case, the one-step decoding is still about 4.5 dB poorer. This 4.5 dB loss is caused by the lSI.

From above results it is seen that different code-lSI channel combinations entail different losses compared with lSI-free, soft-decision decoding case whether onestep decoding or two-step decoding is used. More loss is seen when two-step decoding is used. The coding gain difference between one-step and two-step decodings varies widely (3.8 dB for Example 2 and 2 dB for Example 3). Therefore not much can be said about the difference in general. However, it is evident that the one-step decoding is better than the two-step decoding in terms of error performance.

#### *4.4. Complexity*

The complexity of a Viterbi decoder is basically determined by its number of states.

The two-step decoding needs two Viterbi decoders operating in a pipeline style. The number of states is  $2^v$  for VA1 and  $2^{bm}$  for VA2. In VA1, decoding must be performed on 2<sup> $\text{v}$ </sup> states for every symbol which represents  $R = b/n$  information bits. In VA2, decoding on  $2^{bm}$  states is performed for every *b* information bits. Thus the number of states for each information bit is

$$
S_2 = S_{21} + S_{22} = \frac{2^v}{R} + \frac{2^{bm}}{b}.
$$
 (40)

If decoding is implemented by hardware, VAl and VA2 are operating in a pipeline style, the decoding speed is dictated by the slower one of the two. If decoding is implemented by software, VAl and VA2 are operating serially, the decoding time is the sum of the time used by them.

The one-step decoding needs only one Viterbi decoder. The number of states in the trellis is at most  $2^{bm}$ . Decoding must be performed on  $2^{bm}$  states for every *b* information bits. Thus the number of states for each information bit is

$$
S_1 = \frac{2^{bm'}}{b}.\tag{41}
$$

One-step Viterbi decoder has more states than VA2 since its memory length  $m' = m + \lambda > m$ . It also usually has more states than VA1 ( $2^{bm}$  vs. 2<sup>v</sup>). Consequently its speed usually is slower than the two-step decoding for software implementation. But for hardware implementation, its speed is not necessarily slower if parallel processing is used.

The ratio of  $S_1/S_2$  for Examples 2 and 3 is  $128/72 = 1.778$  which is consistent with the recorded CPU time ratio of 1.771 in the simulation.

#### *V. Conclusion and Discussion*

The WMF receiver structure is adopted to detect convolutionally coded signals with lSI so that an ML decoder using the Viterbi algorithm can be applied. The decoder can be a two-step decoder or a one-step decoder. The two-step decoder consists of a soft-decision Viterbi decoder VAl and a hard-decision Viterbi decoder VA2. The VAl decodes the lSI and noise contaminated signal sequence into a ISIfree and noise-free sequence. The VA2 further corrects the errors in the sequence. The one-step decoder consists only one Viterbi decoder. In order to apply the onestep decoding, an equivalent composite finite state machine model is derived. The bit error probability of the decoders is derived. Two code-channel combinations are evaluated and simulated. The analysis and simulation show that the one-step decoding has better BER than the two-step decoding due to its soft-decision nature. However, the lSI entails loss to the coding gain, so that the coding gain is smaller than that for an lSI-free channel. As to complexity, the onestep decoder is either slower or needs more hardware because it has more states.

Even though the examples studied are in favor of the one-step decoding, the choice between the one-step and two-step decoding need be carefully examined by balancing the error performance and complexity. Also it is seen that the con volutional code must be powerful enough to retain some coding gain in lSI channels. In other words, codes with short constraint length are not suitable for lSI channels.

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